



## UNITED STATES PATENT AND TRADEMARK OFFICE

UNITED STATES DEPARTMENT OF COMMERCE  
 United States Patent and Trademark Office  
 Address: COMMISSIONER FOR PATENTS  
 P.O. Box 1450  
 Alexandria, Virginia 22313-1450  
 www.uspto.gov



Bib Data Sheet

CONFIRMATION NO. 7086

SERIAL NUMBER 10/607,869	FILING DATE 06/27/2003  RULE	CLASS 438	GROUP ART UNIT 2812	ATTORNEY DOCKET NO. MI22-2343
-----------------------------	---------------------------------------	--------------	------------------------	----------------------------------

## APPLICANTS

Zhongze Wang, Boise, ID;

## \*\* CONTINUING DATA \*\*\*\*\*

This application is a DIV of 10/340,126 01/10/2003 PAT 6,864,155  
 which is a DIV of 10/051,981 01/16/2002

yes *JK*yes *JK*

## \*\* FOREIGN APPLICATIONS \*\*\*\*\*

none *JK*

## IF REQUIRED, FOREIGN FILING LICENSE GRANTED

\*\* 09/23/2003

Foreign Priority claimed <input type="checkbox"/> yes <input checked="" type="checkbox"/> no	STATE OR	SHEETS	TOTAL	INDEPENDENT
35 USC 119 (a-d) conditions met <input type="checkbox"/> yes <input checked="" type="checkbox"/> no <input type="checkbox"/> Met after Allowance	COUNTRY	DRAWING	CLAIMS	CLAIMS
Verified and Acknowledged <i>J. Kennedy</i> Examiner's Signature	ID	3	13	1
<i>JK</i> Initials				

## ADDRESS

021567  
 WELLS ST. JOHN P.S.  
 601 W. FIRST AVENUE, SUITE 1300  
 SPOKANE, WA  
 99201

## TITLE

Method of forming silicon-on-insulator comprising integrated circuitry

FILING FEE  RECEIVED 750	FEES: Authority has been given in Paper No. _____ to charge/credit DEPOSIT ACCOUNT No. _____ for following:	<input type="checkbox"/> All Fees <input type="checkbox"/> 1.16 Fees ( Filing ) <input type="checkbox"/> 1.17 Fees ( Processing Ext. of time ) <input type="checkbox"/> 1.18 Fees ( Issue ) <input type="checkbox"/> Other _____ <input type="checkbox"/> Credit
-----------------------------------	---	---